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(DC-03285)

PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Fang Lu and Frank L. Wu  
Filing Date: February 28, 2002  
Title: *Automatic Bios Recovery in a Multi-Node Computer System*

Honorable Assistant Commissioner  
of Patents  
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EL740049553US addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231, on the date shown below.

Katie Powers  
Katie Powers

2-28-02  
Date

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-referenced patent application. Copies of the references are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

Jc903 U.S. PTO  
10/085915  
02/28/02

#2  
IDS  
MAA  
7/3/02

Applicants believe no fees are due, however, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0383 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P.  
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Date: February 28, 2002

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# Information Disclosure Citation in an Application

Application No.

Unknown

Docket Number

016295.0749

Applicant(s)

Fang Lu and Frank L. Wu

Group Art Unit

Unknown

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## U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A	5579522	11/26/96	Christeson et al.	395	652	07/24/95
	B	5634079	05/27/97	Buxton	395	892	05/15/92
	C	5870520	02/09/99	Lee et al.	395	182.04	03/13/96
	D	6003130	12/14/99	Anderson	713	2	10/28/96
	E	6018806	1/25/00	Cortopassi et al.	714	8	10/16/95
	F	6038663	03/14/00	Feldman	713	1	01/16/98
	G	6122733	09/19/00	Christeson et al.	713	2	09/30/97
	H	6161177	12/12/00	Anderson	713	2	10/6/97
	I	6167532	12/26/00	Wisecup	714	23	02/05/98

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J							
	K							

## NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	L	IBM <b>POWER4 System Microarchitecture</b> , at internet < <a href="http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4_7.html">http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4_7.html</a> > Pages 1-3	Printed 1/24/02
	M	IBM <b>Datacenter-class and HPC servers</b> , at internet site < <a href="http://www-1.ibm.com/servers/eserver/pseries/hardware/datactr/">http://www-1.ibm.com/servers/eserver/pseries/hardware/datactr/</a> > Page 1	Printed 1/24/02
	N	PC Accelerate <b>Map to the Intel Chipset Maze: The 820 MCH-f</b> , at internet site < <a href="http://www.pcaccelerate.com/Chipsets/i820-1/i820-1.html">http://www.pcaccelerate.com/Chipsets/i820-1/i820-1.html</a> > Pages 1-3	Printed 1/31/02
	O	Tony Smith, The Register <b>Intel details 16-way DDR-based i870 chipset</b> , at internet site < <a href="http://www.theregister.co.uk/content/archive/21258.html">http://www.theregister.co.uk/content/archive/21258.html</a> > Pages 1-2	Printed 2/27/02
	P	Anthony Cataldo, EE Times <b>Intel prepares for server chip set revival</b> , at internet site < <a href="http://www.eetimes.com/printableArticle?doc_id=OEG20010823S0066">http://www.eetimes.com/printableArticle?doc_id=OEG20010823S0066</a> > Pages 1-3	Printed 2/27/02
	Q	U.S. Patent Application Serial No. 10/027,833 entitled " <b>System and Method for Updating Bios for a Multiple-Node Computer System</b> ," filed by Frank L. Wu et al.; Dell Products L.P. Assignee (DC-03185)	Filed 12/21/01

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.